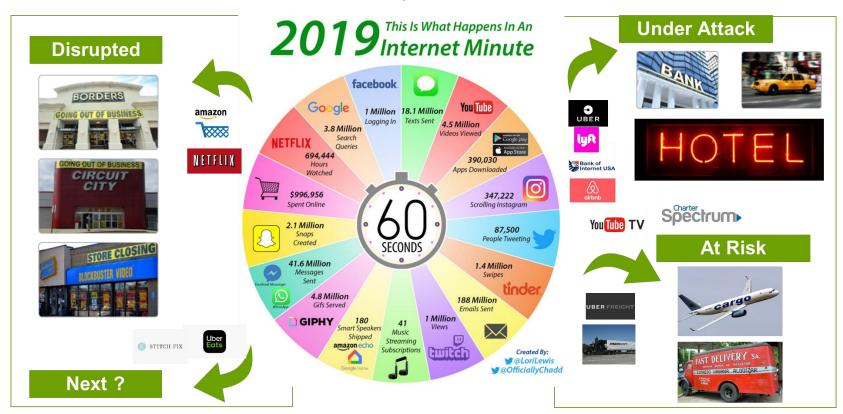


Digital Transformation

IT must be the enabler of the Transformational Journey





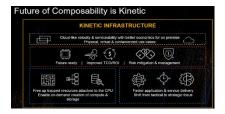
Digital Transformation



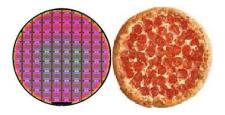
The Edge is Real



Data Science



Future of Composability



Data Consumption



Multi-Cloud



Blockchain



Find your Path



Telemetry & Decision

Automation blogies

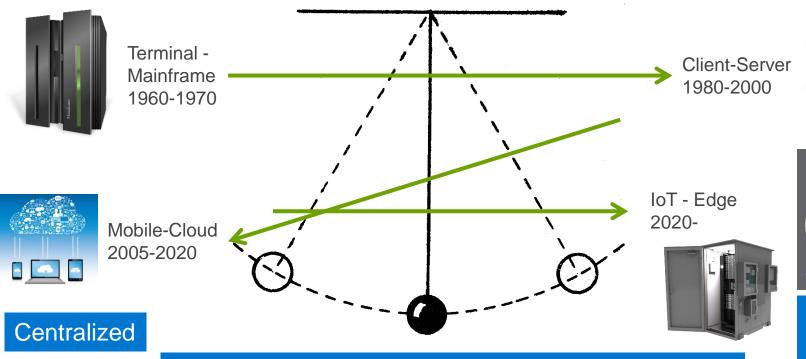


Disruptions happening at every layer

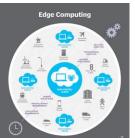
	TODAY	TOMORROW
Users	People	+ Machines, AI, bots
User Experience	Websites	+ AR/VR
Application	Java, .net	+ Microservices, curated, 12 factor, API-driven
Data Management	Relational databases	+ In-memory dbs, memory-centric architectures
Orchestration	Single cloud	+ Multi-cloud
OS/Virtualization	VMs, Microsoft, Linux	+ Uni-kernels, functions, containers, serverless
Network	Discrete: FC Ethernet InfiniBand	+ Open networking, integrated networking, NVMe over fabric
Storage	Block, File	+ Object, key value stores, memory-centric architectures
Compute	X86	+ GPU, FPGA, TPU, IPU, diverse accelerators, ARM, GenZ
Media	SSD HDD DRAM	+ Non-volatile memories, NVMe, SCM

Edge is Changing Compute.....Again

Compute will follow the Data







Distributed Computing

D¢LLTechnologies

F(\$ of compute cycles, \$ of moving data, size of data, data complexity, latency)

STORAGE CLASSE MEMORY (SCM - NVME)

BETWEEN MEMORY AND STORAGE - FASTER THAN SSD. SLOWER THAN DRAM









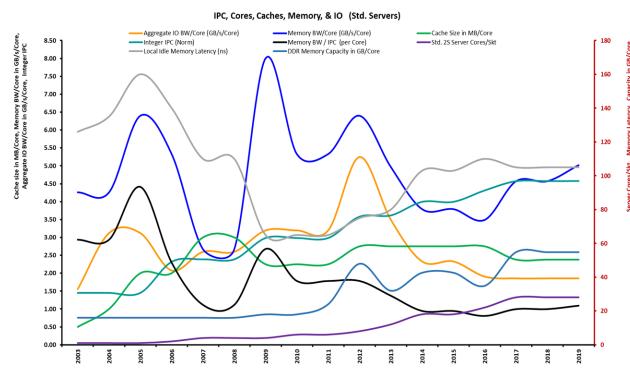


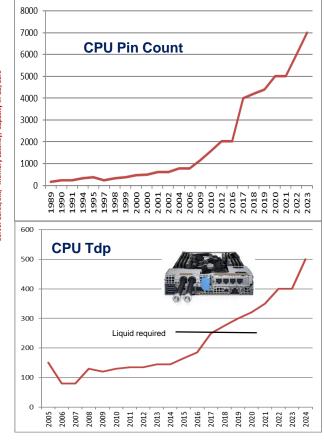


...where & how we spend transistors is changing.

DELLTechnologies

Architectural, Power, Cooling Challenges

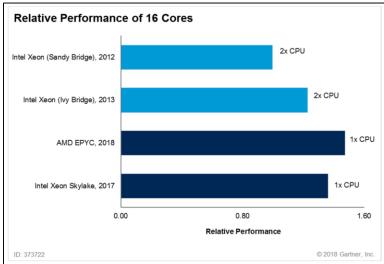


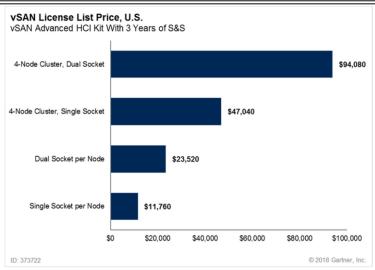


Standard Compute Architectures are not keeping up and POWER problem is back!!!

1S POV

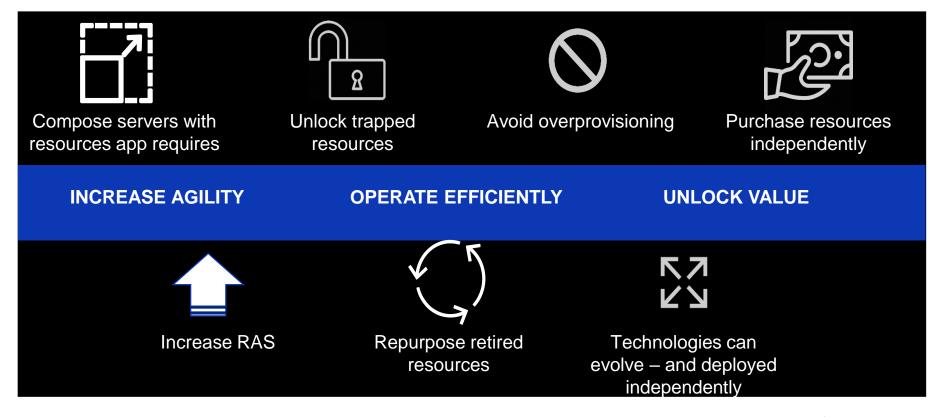
- More than enough cores per socket and trending higher
- Replacement of underutilized 2S servers
- Easier to hit binary channels of memory, and thus binary memory boundaries (128, 256, 512...)
- Lower cost for resiliency clustering (less CPUs/memory....)
- Better software licensing cost for some models
- Avoid NUMA performance hit IO and Memory
- Power density smearing in data center to avoid hot spots
- Repurpose NUMA pins for more channels: DDRx or PCle or future buses (CxL, Gen-Z)
- Enables better NVMe direct drive connect without PCIe Switches
- Gartner agrees and did a paper. (<u>https://www.gartner.com/doc/reprints?id=1-680TWAA&ct=190212&st=sb</u>)







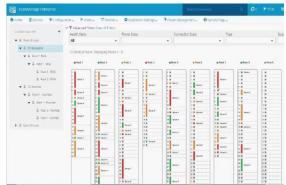
Resource Optimizations (Kinetic Infrastructure)

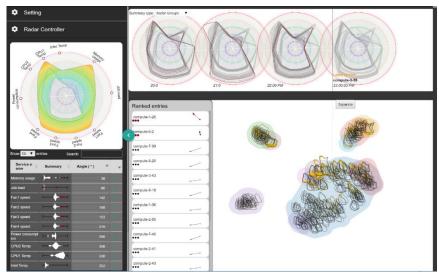




Cognitive Telemetry & AI for Monitoring and Management

- iDRAC-gathered high value telemetry data
- Create on-prem "data lake"
 - Hardware Sensors, Performance
- Visualization layout across server, rack, datacenter (core to the edge)
- Health data visualization across
- Provide additional scope for intelligent management using relevant data





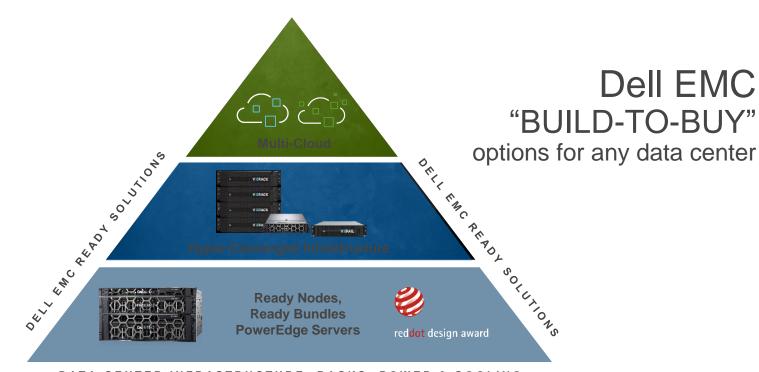
APPLICATIONS

HPC

DATA **ANALYTICS**

BUSINESS APPLICATIONS

> **SOFTWARE DEFINED**



DATA CENTER INFRASTRUCTURE: RACKS, POWER & COOLING

Dell EMC

85%

of **CIOs** are investing in Artificial Intelligence in the next three years

By 2020, 20% of the enterprise infrastructures deployed will be used for Al.. up from 3% in 2017

IT industry trends



SOFTWARE DEFINED DATA CENTER



WORKLOAD ACCELERATION



EDGE COMPUTING/IoT



ARTIFICIAL INTELLIGENCE



PROCESS CHALLENGES



D LLTechnologies